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QD4056

1.1A Lithium Ion Battery Linear Charger

Features

- Programmable charge current up to 1.1A
- No MOSFET sense resistor or blocking diode required
- Complete linear Charger for single Cell Lithium-Ion Batteries
- CC and CV operation with thermal regulation to maximize Rate without risk of overheating
- Preset 4.2V charge voltage with $\pm 1\%$ accuracy
- Automatic Recharge
- Two Status Indication for Charge status, no battery and battery failure indicators
- C/10 charge termination
- 55 μ A supply current in shutdown
- 2.9V trickle current charge threshold
- Soft-Start limits inrush current
- Battery Temperature Sensing
- Available in SOP-8 and ESOP-8 package

Applications

- Cellular Telephones
- Mobile Power Supply
- Digital Still Cameras
- MP3 Players
- Bluetooth Applications
- Portable Devices
- USB Bus-Powered Chargers

GENERAL DESCRIPTION

QD4056 is a complete CC/CV linear charger for single cell lithium-ion batteries. it is specifically designed to work within USB power Specifications.

No external sense resistor is needed and no blocking diode is required due to the internal P-MOSFET architecture. Thermal feedback regulates the charge current to limit the die temperature during high power operation or high ambient temperature .The charge voltage is fixed at 4.2V, and the charge current can be programmed externally with a single resistor. The QD4056 automatically terminates the charge cycle when the

charge current drops to 1/10th the programmed value after the final float voltage is reached.

When the input supply (wall adapter or USB supply) is removed the QD4056 automatically enters a low current state dropping the battery drain current to less than 2 μ A. The QD4056 can be put into shutdown mode reducing the supply current to 55 μ A.

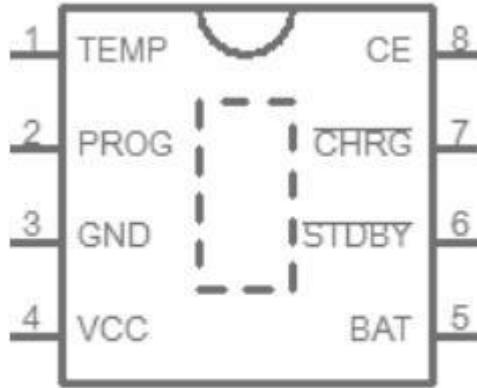
Other features include Battery temperature monitor, under-voltage lockout, automatic recharge and two status pins to indicate charge and charge termination.



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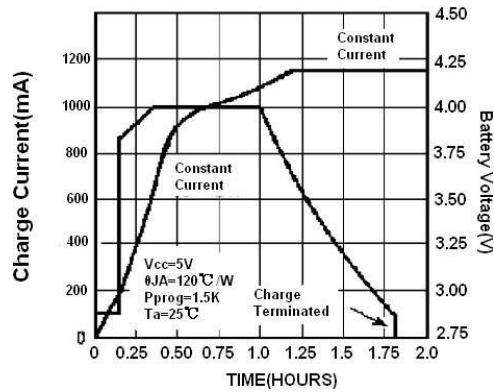
Pin Configuration



(PAD is GND)

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Typical charge cycle (1100 mAh Battery)



Pin Assignment

PIN Num.	Symbol	Function
1	TEMP	<p>Temperature sense input</p> <p>Connecting TEMP pin to NTC thermistor 's output in Lithium ion battery pack. If TEMP pin's voltage is below 45% or above 80% of supply voltage VCC, this means that battery's temperature is too low or too high, charging is suspended. The temperature sense function can be disabled by grounding the TEMP pin.</p>
2	PROG	<p>Constant Charge Current Setting and Charge Current Monitor Pin</p> <p>The charge current is programmed by connecting a resistor RPROG from this pin to GND. When in precharge mode, the PROG pin 's voltage is regulated to 0.1V. When charging in constant-current mode this pin 's voltage is regulated to 1V. In all modes during charging, the voltage on this pin can be used to measure the charge current using the following formula:</p> $I_{PROG} = \frac{V_{PROG}}{R_{PROG}} \times 1200$
3	GND	Ground
4	VCC	<p>Positive input supply voltage</p> <p>Provides power to the internal circuit. When VCC drops to within 80mV of the BAT pin voltage, the QD4056 enters low power sleep mode, dropping IBAT to less than 2μA.</p>
5	BAT	<p>Battery connection Pin</p> <p>Connect the positive terminal of the battery to this pin. Dropping BAT pin's current to less than 2μA when IC in disable mode or in sleep mode. BAT pin provides charge current to the battery and provides regulation voltage of 4.2V.</p>
6	\overline{STDBY}	<p>Charge terminated status output</p> <p>is pulled low by an internal switch to indicate a battery charge terminated; this means Charge termination. Otherwise pin is in high impedance \overline{STDBY} state.</p>
7	\overline{CHRG}	<p>Open-Drain charge status output</p> <p>When the battery is being charged, the \overline{CHRG} pin is pulled low by an internal switch, otherwise, \overline{CHRG} pin is in high impedance state.</p>
8	CE	<p>Chip enable input</p> <p>A high input will put the device in the normal operating mode. Pulling the CE pin to low level will put the QD4056 into disable mode. The CE pin can be driven by TTL or CMOS logic level.</p>

Absolute Maximum Ratings

Parameter	Rating	Unit
Input supply voltage : VCC	-0.3~8	V
PROG pin voltage	-0.3~VCC+0.3	V
BAT pin voltage	-0.3~7	V
TEMP pin voltage	-0.3~10	V
pin voltage	-0.3~10	V
pin voltage	-0.3~10	V
CE pin voltage	-0.3~10	V
BAT pin current	1200	mA
PROG pin current	1200	uA
Maximum junction temperature	150	°C
Operating ambient temperature	-40~85	°C
Storage temperature	-65~125	°C
Soldering temperature and time	+260 (Recommended 10S)	°C

Caution: The absolute maximum ratings are rated values exceeding which the product could suffer physical damage. These values must therefore not be exceeded under any conditions.

Electrical Characteristics

Symbol	Parameter	Test Condition	MIN	TPY	MAX	Unit
Vcc	Input supply voltage		4.0	5.0	8.0	V
ICC -IBAT	Static Current	Charge mode, RPROG=1.3K		150	500	uA
		Standby mode (charge end)		55	100	uA
		Shutdown mode RPROG not connected, VCC<VBAT, or VCC<VUV)		55	100	uA
VFLOAL	Regulated output voltage	0°C≤TA≤85°C IBAT=40mA	4.158	4.2	4.242	V
IBAT	BAT pin current (The condition of current mode is VBAT=3.9V)	RPROG=2.6K, current mode	525	550	575	mA
		RPROG=1.2K, current mode	1050	1100	1150	mA
		Standby mode: VBAT=4.2V	0	-2.5	-6.0	uA
		Shutdown mode, RPROG not connected		±1	±2	uA
		Sleep mode, VCC=0V		-1	-2	uA
ITRIKL	Trickle charge current	VBAT<VTRIKL, RPROG=1.3K	120	130	140	mA
VTRIKL	Trickle charge threshold voltage	RPROG=1.3K, VBAT rising	2.8	2.9	3.0	V
VTRHYS	Trickle voltage hysteresis voltage	RPROG=1.3K	150	200	250	mV
VUV	VCC under voltage	VCC from low to high	3.5	3.7	3.9	V



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	lockout threshold					
VUVHYS	VCC under voltage lockout hysteresis		150	200	300	mV
VASD	VCC-VBAT lockout threshold voltage	VCC from low to high	100	140	180	mV
		VCC from high to low	50	80	110	mV
ITERM	C/10 termination current threshold	RPROG=2.6K	60	70	80	mA
		RPROG=1.2K	120	130	140	mA
VPROG	PROG pin voltage	RPROG=1.3K, current mode	0.9	1.0	1.1	V
$V_{\overline{CHRG}}$	\overline{CHRG} Pin output low voltage	$I_{\overline{CHRG}} = 5mA$		0.3	0.6	V
$V_{\overline{STDBY}}$	\overline{STDBY} Pin output low voltage	$I_{\overline{STDBY}} = 5mA$				
T_{TEMP-H}	The voltage at TEMP increase			80	83	%VCC
T_{TEMP-L}	The voltage at TEMP decrease		42	45		%VCC
$\Delta V_{RECHARGE}$	Recharge battery threshold voltage	VFLOAT -VRECHRG	120	180	240	mV
T_{LIM}	Thermal protection temperature			145		°C
$R_{DS(ON)}$	The resistance of power FET "ON" (between VCC and BAT)			650		mΩ
T_{SS}	Soft-start time	IBAT=0 to IBAT=1300V/RPROG		20		μS
$T_{RECHARGE}$	Recharge comparator filter time	VBAT from high to low	0.8	1.8	4	mS
T_{TEMP}	Termination comparator filter time	IBAT below ICHG/10	0.8	1.8	4	mS
I_{PROG}	PROG pin pull-up current			2.0		μA

Note: The ● denotes specifications which apply over the full operating temperature rang, otherwise specifications are at TA=25°C, VCC=5V, unless otherwise specified.

Description of the Principle

The QD4056 is a complete CC/CV linear charger for single cell lithium-ion batteries. CC/CV to charger batter by internal MOSFET .It can deliver up to 1.1A of charge current .No blocking diode or external current sense resistor is required.

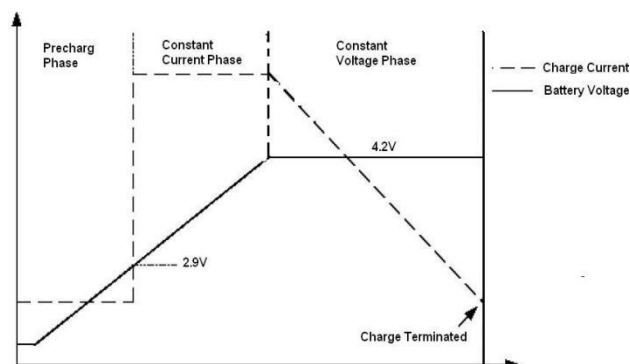
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QD4056 include two Open-Drain charge status Pins: Charge status indicator \overline{CHRG} and battery failure status output \overline{STDBY} .

The internal thermal regulation circuit reduces the programmed charge current if the die temperature attempts to rise above a preset value of approximately 145°C. This feature protects the QD4056 from excessive temperature, and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the QD4056 or the external components. Another benefit of adopting thermal regulation is that charge current can be set according to typical, not worst-case, ambient temperatures for a given application with the assurance that the charger will automatically reduce the current in worst-case conditions.

The charge cycle begins when the voltage at the VCC pin rises above the UVLO level, a current set resistor is connected from the PROG pin to ground, and the CE pin is pulled above the chip enable threshold. The \overline{CHRG} pin outputs a logic low to indicate that the charge cycle is on going. At the beginning of the charge cycle, if the battery voltage is below 2.9V, the charge is in precharge mode to bring the cell voltage up to a safe level for charging. The charger goes into the fast charge CC mode once the voltage on the BAT pin rises above 2.9 V. In CC mode, the charge current is set by RPROG. When the battery approaches the regulation voltage 4.2V, the charge current begins to decrease as the QD4056 enters the CV mode. When the current drops to charge termination threshold, the charge cycle is terminated, and \overline{CHRG} pin assumes a high impedance state to indicate that the charge cycle is terminated and \overline{STDBY} pin is pulled low. The charge termination threshold is 10% of the current in CC mode. To restart the charge cycle, remove the input voltage and reapply it, or momentarily force CE pin to 0V. The charge cycle can also be automatically restarted if the BAT pin voltage falls below the recharge threshold. The on-chip reference voltage, error amplifier and the resistor divider provide regulation voltage with 1% accuracy which can meet the requirement of lithium-ion and lithium polymer batteries. When the input voltage is not present, or input voltage is below VBAT, the charger goes into a sleep mode, dropping battery drain current to less than 3μ A. This greatly reduces the current drain on the battery and increases the standby time. The charger can be shutdown by forcing the CE pin to GND.

The charging profile is shown in the following figure:



Programming Charge Current

The charge current is programmed using a single resistor from the PROG pin to ground. The program resistor and the charge current are calculated using the following equations.

$$R_{PROG} = \frac{1200}{I_{BAT}};$$

In application, according to the charge current to determine RPROG, the relation between RPROG and charge current can reference the following chart:

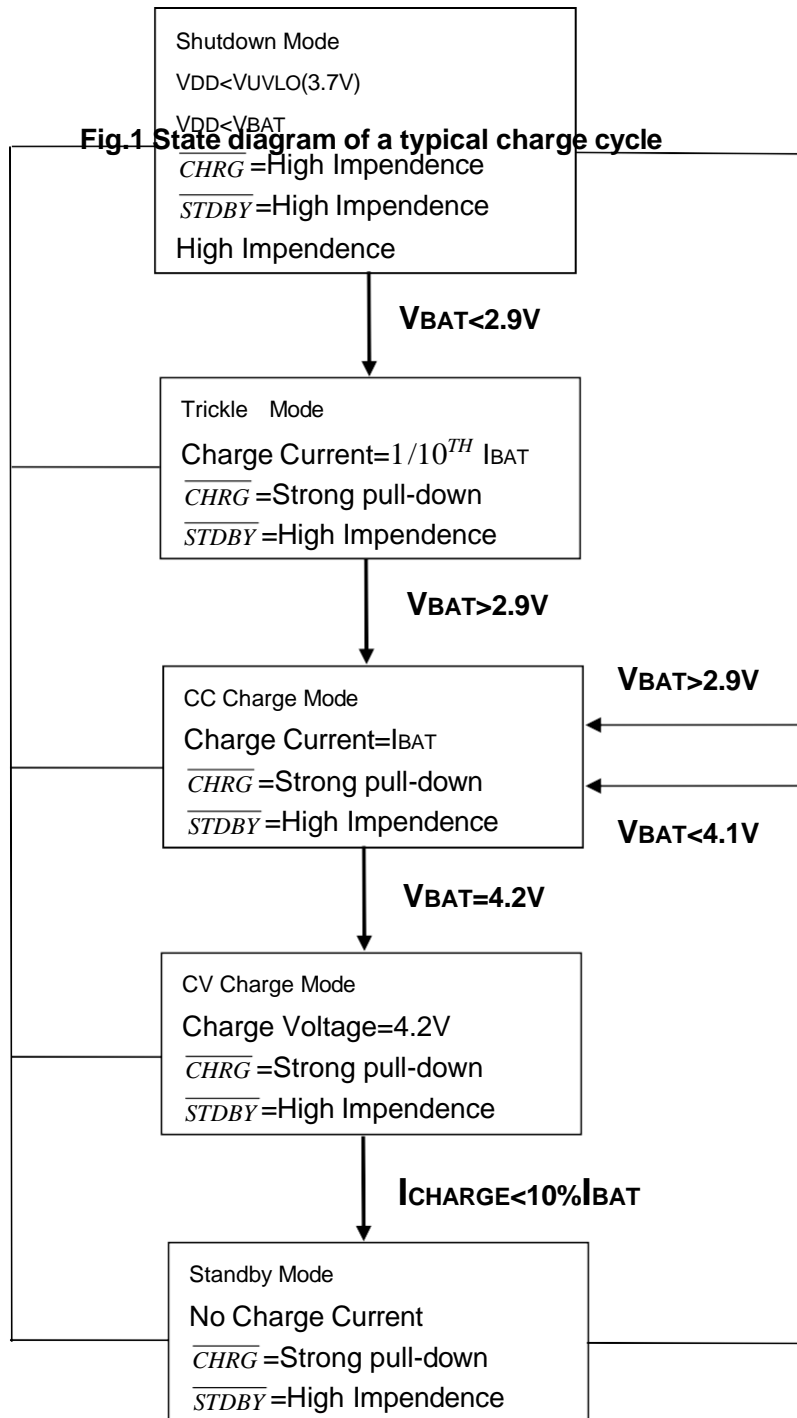
RPROG (K)	IBAT (mA)
24K	50
12K	100
4K	300
3K	400
2K	600
1.5K	800
1.2K	1000

Charge Termination

A charge cycle is terminated when the charge current falls to 1/10th the programmed value after the final float voltage is reached. This condition is detected by using an internal filtered comparator to monitor the PROG pin. When the PROG pin voltage falls below 100mV for longer than tTEMP (typically 1.8mS), Charging is terminated. The charge current is latched off and the QD4056 enters standby mode, where the input supply current drops to 55 μ A (**Note:C/10 termination is disabled in trickle charging and thermal limiting modes**).

When charging, transient loads on the BAT pin can cause the PROG pin to fall below 100mV for short periods of time before the DC charge current has dropped to 1/10th the programmed value. The 1.8mS filter time (tTEMP) on the termination comparator ensures that transient loads of this nature do not result in premature charge cycle termination. Once the average charge current drops below 1/10th the programmed value, the QD4056 terminated the charge cycle and ceases to provide any current through the BAT pin. In this state all loads on the BAT pin must be supplied by the battery.

The QD4056 constantly monitors the BAT pin voltage in standby mode. If this voltage drops below the 4.10V recharge threshold (VRECHRG), another charge cycle begins and current is once again supplied to the battery. To manually restart a charge cycle when in standby mode, the input voltage must be removed and reapplied or the charger must be shut down and restarted using the PROG pin. Figure 1 shows the state diagram of a typical charge cycle



Thermal limiting

An internal thermal feedback loop reduces the programmed charge current if the die temperature attempts to rise above a preset value of approximately 145°C. The feature protects the QD4056 from excessive temperature and allows the user to push the limits of the power handling capability of a given circuit board without risk of damaging the QD4056. The charge current can be set according to typical (not worst-case) ambient temperature with the assurance that the charger will automatically reduce the current in worst-case conditions.

To prevent the damage caused by the very high or very low temperature done to the battery pack, the QD4056 continuously senses battery pack temperature by measuring the voltage at TEMP pin determined by the voltage divider circuit and the battery's internal NTC thermistor as shown in Figure 1.

The QD4056 compares the voltage at TEMP pin (VTEMP) against its internal VLOW and VHIGH thresholds to determine if charging is allowed. In QD4056, VLOW is fixed at (45%×Vcc), while VHIGH is fixed at (80%×Vcc). If VTEMP<VLOW or VTEMP>VHIGH, it indicates that the battery temperature is too high or too low and the charge cycle is suspended. When VTEMP is between VLOW and VHIGH, charge cycle resumes. The battery temperature sense function can be disabled by connecting TEMP pin to GND.

Under Voltage lockout (UVLO)

An internal under voltage lockout circuit monitors the input voltage and keeps the charger in shutdown mode until VCC rises above the under voltage lockout threshold. If the UVLO comparator is tripped, the charger will not come out of shutdown mode until VCC rises 140mV above the battery voltage.

Auto restart

Once charge is been terminated, QD4056 immediately use a 1.8ms filter time (tRECHARGE) on the termination comparator to constant monitor the voltage on BAT pin. If this voltage drops below the 4.1V recharge threshold (about between 80% and 90% of VCC), another charge cycle begins. This ensured the battery maintained (or approach) to a charge full status and avoid the requirement of restarting the periodic charging cycle. In the recharge cycle, \overline{CHRG} pin enters a pulled down status.

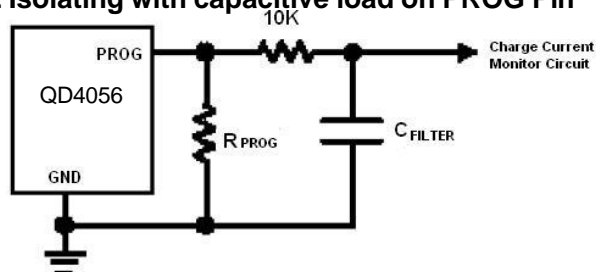
Stability Considerations

In CC mode, the PROG pin is in the feedback loop, not the battery. The CC mode stability is affected by the impedance at the PROG pin. With no additional capacitance on the PROG pin, the charger is stable with program resistor values as high as 20K. However, additional capacitance on this node reduces the maximum allowed program resistor. Therefore, if IPROG pin is loaded with a capacitance C, the following equation should be used to calculate the maximum resistance

value for RPROG:
$$R_{PROG} \leq \frac{1}{2\pi \times 10^5 \times C_{PROG}}$$

As user, may think charge current is important, not instantaneous current. For example, to run a low current mode switch power which parallel connected with battery, the average current from BAT pin usually importance to instantaneous current. In this case, In order to measure average charge current or isolate capacitive load from IPROG pin, a simple RC filter can be used on PROG pin as shown in Figure 2. In order to ensure the stability add a 10K resistor between PROG pin and filter capacitor.

Fig.2 Isolating with capacitive load on PROG Pin



Power Dissipation

The conditions that cause the QD4056 to reduce charge current through thermal feedback can be approximated by considering the power dissipated in the IC. Nearly all of this power dissipation is generated by the internal MOSFET-this is calculated to be approximately:

$P_D = (V_{CC} - V_{BAT}) \times I_{BAT}$ The approximate ambient temperature at which the thermal feedback begins to protect the IC

is:

$$T_A = 145^\circ C - P_D \times \theta_{JA}; \quad \text{So: } T_A = 145^\circ C - (V_{CC} - V_{BAT}) \times I_{BAT} \times \theta_{JA}$$

For example: The QD4056 with 5V supply voltage through programmable provides full limiting current 850mA to a charge lithium-ion battery with 3.85V voltage. If θ_{JA} is $100^\circ C/W$ (reference to PCB layout considerations), When QD4056 begins to decrease the charge current, the ambient temperature about:

$$T_A = 145 - (5 - 3.85) \times 0.85 \times 100 = 47.25^\circ C$$

QD4056 can work in the condition of the temperature is above $47.25^\circ C$, but the charge current will pull down to below 850mA. In a fixed ambient temperature, the charge current is calculated to be approximately :

$$I_{BAT} = \frac{145^\circ C - T_A}{(V_{CC} - V_{BAT}) \times \theta_{JA}}$$

Just as Description of the Principle part talks about so, the current on PROG pin will reduce in proportion to the reduced charge current through thermal feedback. In QD4056 design applications don't need to considerate the worst case of thermal condition, this point is importance, because if the junction temperature up to $145^\circ C$, QD4056 will auto reduce the power dissipation.

Thermal Considerations

Because of the small size of the thin SOP-8 or ESOP-8 package, it is important to use a good thermal PC board layout to maximize the available charge current. The PC board copper is the heat sink. The footprint copper pads should be as wide as possible and expand out to larger copper areas to spread and dissipate the heat to the surrounding ambient. Other heat sources on the board, not related to the charger, must also be considered when designing a PC board layout because they will affect overall temperature rise and the maximum charge current.

VCC bypass capacitor

Many types of capacitors can be used for input bypassing, however, caution must be exercised when using multilayer ceramic capacitors. Because of the self-resonant and high Q characteristics of some types of ceramic capacitors, high



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voltage transients can be generated under some start-up conditions, such as connecting the charger input to a live power source. Adding a 1.5Ω resistor in series with a ceramic capacitor will minimize start-up voltage transients.

Charging Current Soft Start

QD4056 includes a soft start circuit which used to maximize to reduce the surge current in the begging of charge cycle. When restart a new charge cycle, the charging current ramps up from 0 to the full charging current within 20μs. In the start process it can maximize to reduce the action which caused by surge current load.

Board Layout Considerations

RPROG at PROG pin should be as close to QD4056 as possible, also the parasitic capacitance at PROG pin should be kept as small as possible.

The capacitance at VCC pin and BAT pin should be as close to QD4056 as possible.

During charging, QD4056's temperature may be high, the NTC thermistor should be placed far enough to QD4056 so that the thermistor can reflect the battery's temperature correctly.

It is very important to use a good thermal PC board layout to maximize charging current. The thermal path for the heat generated by the IC is from the die to the copper lead frame through the package lead (especially the ground lead) to the PC board copper, the PC board copper is the heat sink. The footprint copper pads should be as wide as possible and expand out to larger copper areas to spread and dissipate the heat to the surrounding ambient. Feed through vias to inner or backside copper layers are also useful in improving the overall thermal performance of the charger. Other heat sources on the board, not related to the charger, must also be considered when designing a PC board layout because they will affect overall temperature rise and the maximum charge current.

The ability to deliver maximum charge current under all conditions require that the exposed metal pad on the back side of the QD4056 package be soldered to the PC board ground. Failure to make the thermal contact between the exposed pad on the backside of the package and the copper board will result in larger thermal resistance.

Add thermal regulation current

It will effective to decrease the power dissipation through reduce the voltage of both ends of the inner MOSFET. In the thermal regulation, this action of transporting current to battery will raise. One of the measure is through an external component(as a resistor or diode) to consume some power dissipation.

For example: The QD4056 with 5V supply voltage through programmable provides full limiting current 1000mA to a charge lithium-ion battery with 3.8V voltage. If θJA is 120°C/W, so that at 25°C ambient temperature, the charge current is calculated to be approximately :

$$I_{BAT} = \frac{145^{\circ}C - 25^{\circ}C}{(V_{CC} - I_{BAT} \times R_{CC} - V_{BAT}) \times \theta_{JA}}$$

In order to increase the thermal regulation charge current, can decrease the power dissipation of the IC through reducing the voltage (as show fig.3) of both two ends of the resistor which connecting in series with a 5V AC adapter. With square equation to calculate I BAT :

$$I_{BAT} = \frac{V_{CC} - V_{BAT} - \sqrt{(V_{CC} - V_{BAT})^2 - \frac{4R_{CC} \times (145^{\circ}C - T_A)}{\theta_{JA}}}}{2R_{CC}}$$

If RCC=0.25Ω, VCC=5V, VBAT=3.75V, TA=25 °C and θJA=120 °C/W, we can calculate the thermal regulation charge current: IBAT = 1080mA. It means that in this structure it can output 1000mA full limiting charge current at more high



ambient temperature environment.

Although it can transport more energy and reduce the charge time in this application, but actually spread charge time, if QD4056 stay in under-voltage state, when VCC becomes too low in voltage mode. Fig.4 shows how the voltage reduced with increase RCC value in this circuit. This technique will act the best function when in order to maintain the minimize the dimension of the components and avoid voltage decreased to minimize RCC .

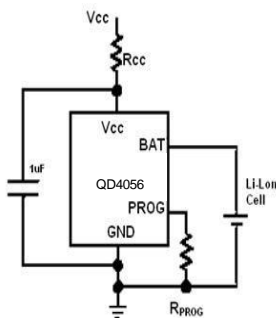


Fig.3 A Circuit To Maximum The Thermal Regulation Charge Current

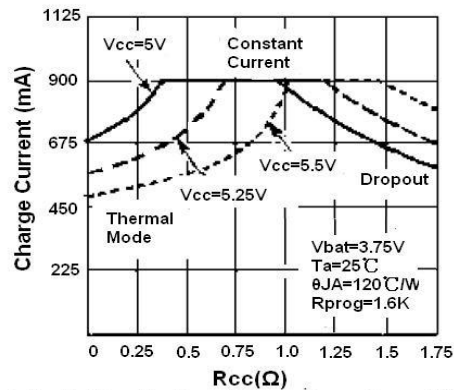


Fig.4 The Relationship Curve Between Charge Current With Rcc

USB and Wall Adapter Power

QD4056 allows charging from a USB port, a wall adapter can also be used to charge Li-Ion/Li-polymer batteries. Figure 5 shows an example of how to combine wall adapter and USB power inputs. A P-channel MOSFET, Q1, is used to prevent back conducting into the USB port when a wall adapter is present and Schottky diode, D1, is used to prevent USB power loss through the 1KΩ pull-down resistor.

Generally, AC adaptor is able to provide bigger much current than the value of specific current limiting which is 500mA for USB port. So can rise charge current to 600mA with using a N-MOSFET (Q1) and an additional set resistor value as high as 10K.

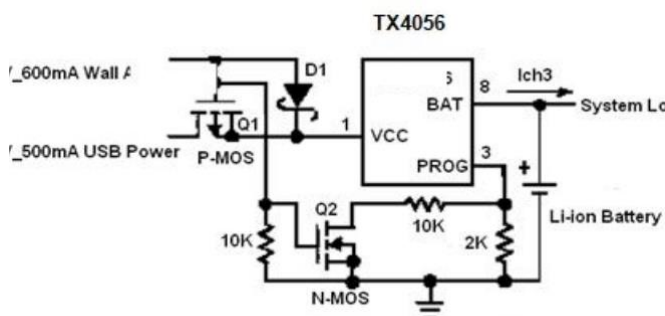


Fig.5:Combining Wall Adapter and USB Power

Charge status indicator

QD4056 has two open-drain status indicator output \overline{CHRG} and \overline{STDBY} . \overline{CHRG} is pull-down when the QD4056 in a charge cycle. In other status \overline{CHRG} in high impedance, \overline{CHRG} and \overline{STDBY} are all in high impedance when the battery out of the normal temperature.

Represent in failure state, when TEMP pin in typical connecting, or the charger with no battery: red LED and green LED all don't light. The battery temperature sense function is disabled by connecting TEMP pin to GND. If battery is not connected to charger, \overline{CHRG} pin outputs a PWM level to indicate no battery. If BAT pin connects a 10 μF capacitor, the frequency of \overline{CHRG} flicker about 1-4S, If not use status indicator should set status indicator output connected to GND.

Charger's Status	Red led \overline{CHRG}	Green led \overline{STDBY}
Charging	light	dark
Battery in full state	dark	light
Under-voltage, battery's temperature is to high or too low, or not connect to battery(use TEMP)	dark	dark
BAT pin is connected to 10uF capacitor, No battery mode (TEMP=GND)	Green LED bright, Red LED flicker F=1-4 S	

Selecting R1 and R2

The values of R1 and R2 in the application circuit can be determined according to the assumed temperature monitor range and thermistor's values. The Follows is an example: Assume temperature monitor range is TL ~TH, (TL < TH); the thermistor in battery has negative temperature coefficient (NTC), RTL is thermistor's resistance at TL, RTH is the resistance at TH, so RTL > RTH, then at temperature TL, the voltage at TEMP pin is:

$$V_{TEMPH} = \frac{R_2 // R_{TH}}{R_1 + R_2 // R_{TH}} \times V_{IN}$$

At temperature TH, the voltage at TEMP pin is:

$$V_{TEMPL} = \frac{R_2 // R_{TL}}{R_1 + R_2 // R_{TL}} \times V_{IN}$$

We know VTEMPL = VHIGH = K2 × Vcc (K2 = 0.8); VTEMPH = VLOW = K1 × Vcc (K1 = 0.45) Then we can have:

$$R_1 = \frac{R_{TL} \times R_{TH} (K_2 - K_1)}{(R_{TL} - R_{TH}) K_2 \times K_1}; \quad R_1 = \frac{R_{TL} \times R_{TH} (K_2 - K_1)}{R_{TL} (K_1 - K_1 \times K_2) - R_{TH} (K_2 - K_1 \times K_2)}$$

Likewise, for positive temperature coefficient thermistor in battery, we have RTH > RTL and we can calculate:

$$R_1 = \frac{R_{TL} \times R_{TH} (K_2 - K_1)}{(R_{TH} - R_{TL}) K_2 \times K_1}; \quad R_1 = \frac{R_{TL} \times R_{TH} (K_2 - K_1)}{R_{TH} (K_1 - K_1 \times K_2) - R_{TL} (K_2 - K_1 \times K_2)}$$

We can conclude that temperature monitor range is independent of power supply voltage VCC and it only depends on R1, R2, RTL and RTH: The values of RTH and RTL can be found in related battery handbook or deduced from testing data. In actual application, if only one terminal temperature is concerned (normally protecting overheating), there is no need to use R2 but R1. It becomes very simple to calculate R1 in this case.

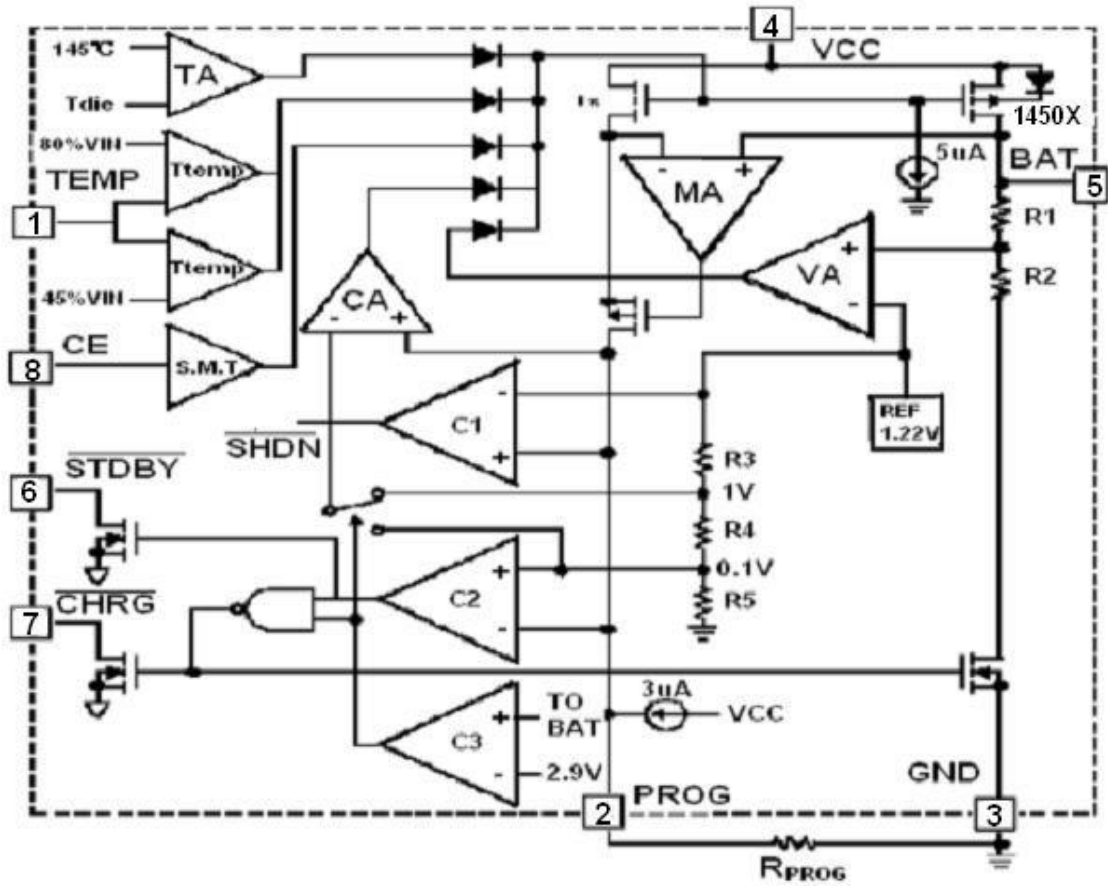


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Block Diagram



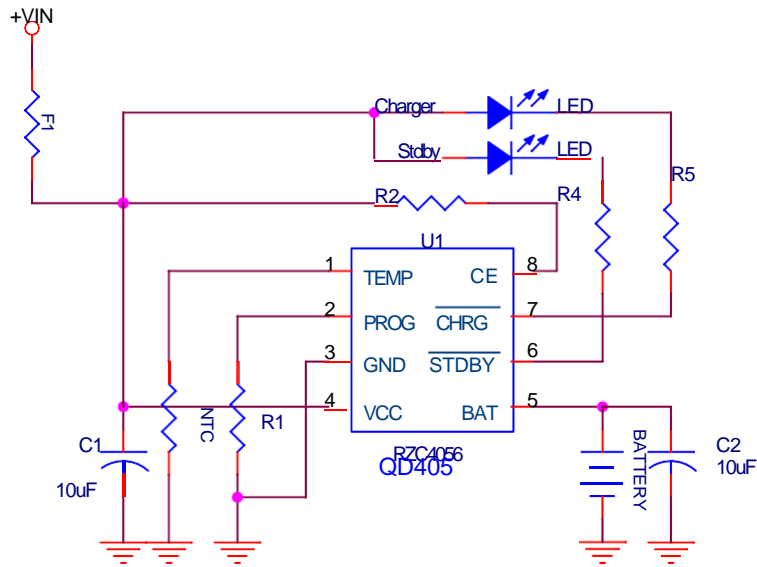


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Typical Application





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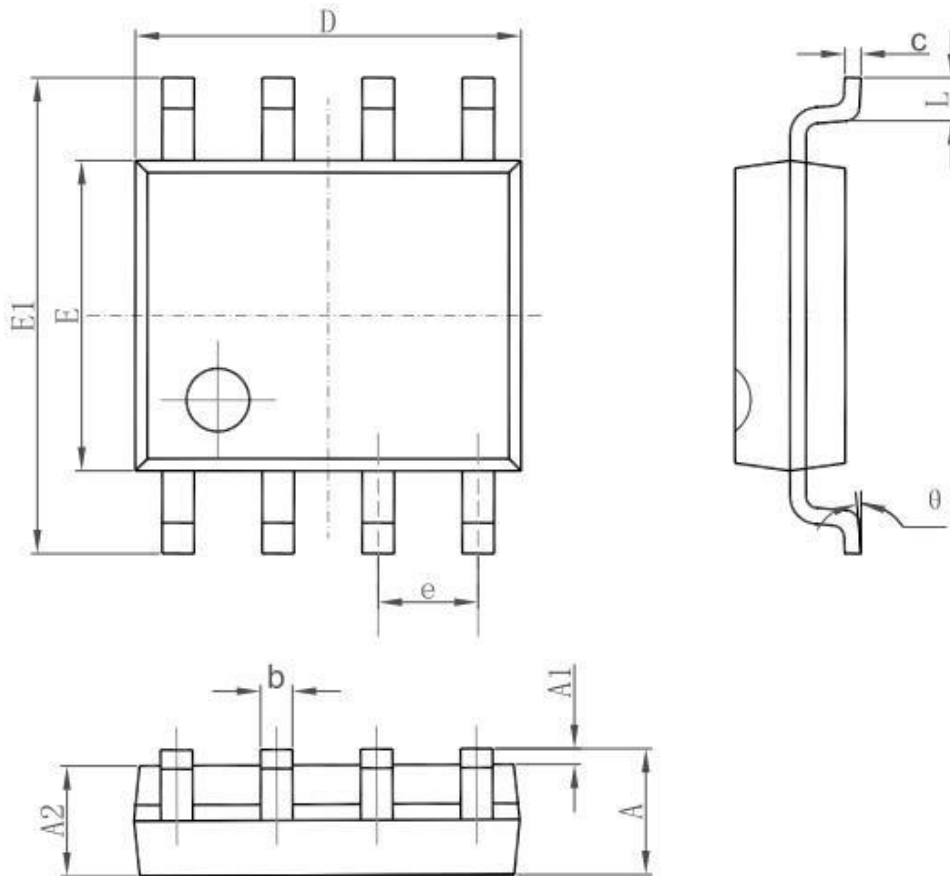
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Packaging Information:

SOP8 PACKAGE OUTLINE DIMENSIONS



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.100	0.250	0.004	0.010
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.006	0.010
D	4.700	5.100	0.185	0.200
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



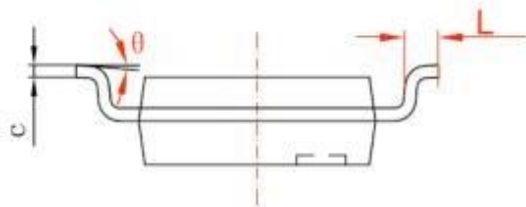
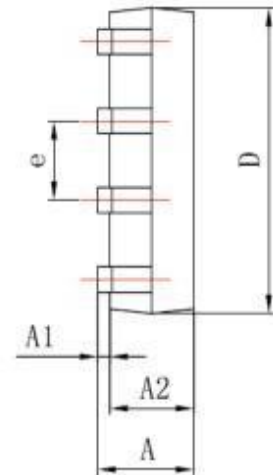
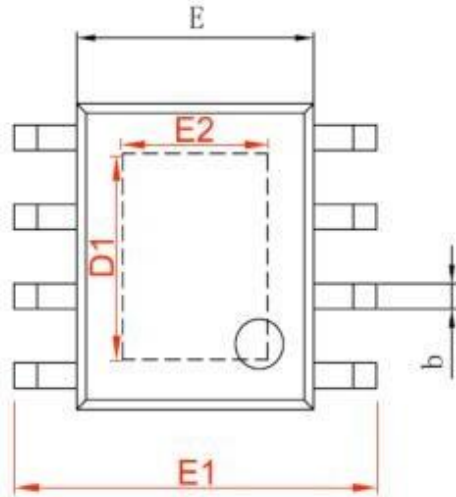
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1.1A Lithium Ion Battery Linear Charger

eSOP8 PACKAGE OUTLINE DIMENSIONS



字符	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	1.350	1.750	0.053	0.069
A1	0.050	0.150	0.002	0.006
A2	1.350	1.550	0.053	0.061
b	0.330	0.510	0.013	0.020
c	0.170	0.250	0.007	0.010
D	4.700	5.100	0.185	0.200
D1	3.202	3.402	0.126	0.134
E	3.800	4.000	0.150	0.157
E1	5.800	6.200	0.228	0.244
E2	2.313	2.513	0.091	0.099
e	1.270 (BSC)		0.050 (BSC)	
L	0.400	1.270	0.016	0.050
θ	0°	8°	0°	8°



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1.1A Lithium Ion Battery Linear Charger

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